**Integration Manual**

**For**

**State Output Control**

**VERSION: 3.0**

**DATE: 05-AUG-2015**

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**Revision History**

|  |  |  |  |
| --- | --- | --- | --- |
| **Rev #** | **Change Description** | **Date** | **Author** |
| 1 | Initial version | 30-Jan-15 | SB |
| 2 | Updated SF-05 v004 based on FDD v4.0.0 | 10-Apr-15 | KK |
| 3 | Updated to FDD SF005A\_4.1.0 | 05-Aug-15 | JK |

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# Abbrevations And Acronyms

|  |  |
| --- | --- |
| **Abbreviation** | **Description** |
| DFD | Design functional diagram |
| MDD | Module design Document |

# References

This section lists the title & version of all the documents that are referred for development of this document

|  |  |  |
| --- | --- | --- |
| **Sr. No.** | **Title** | **Version** |
| 1 | SF05A State Output Control | 4.1.0 |

# Dependencies

## SWCs

|  |  |
| --- | --- |
| **Module** | **Required Feature** |
| **None** | <Addition of global data, function>\*. |

Note : Referencing the external components should be avoided in most cases. Only in unavoidable circumstance external components should be referred. Developer should track the references.

## Global Functions(Non RTE) to be provided to Integration Project

None

# Configuration REQUIREMeNTS

## Build Time Config

|  |  |  |
| --- | --- | --- |
| **Modules** | **Notes** |  |
| **None** |  |  |

## Configuration Files to be provided by Integration Project

## Da Vinci Parameter Configuration Changes

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Notes** | **SWC** |
| **None** |  |  |

## DaVinci Interrupt Configuration Changes

|  |  |  |  |
| --- | --- | --- | --- |
| **ISR Name** | **VIM #** | **Priority Dependency** | **Notes** |
| **None** |  |  |  |

## Manual Configuration Changes

|  |  |  |
| --- | --- | --- |
| **Constant** | **Notes** | **SWC** |
| **None** |  |  |

# Integration DATAFLOW REQUIREMENTS

## Required Global Data Inputs

DiagRampRate\_XpmS\_f32

DiagRampValue\_Uls\_f32

DiagStsDiagRmpActive\_Cnt\_lgc

OperRampRate\_XpmS\_f32

OperRampValue\_Uls\_f32

RampSrlComSvcDft\_Cnt\_lgc

LoaRateLimit\_UlspS\_f32

LoaScaleFctr\_Uls\_f32

StrtStopRateLimit\_UlspS\_f32

StrtStopScaleFctr\_Uls\_f32

## Required Global Data Outputs

OutputRampMult\_Uls\_f32

## SysStReqDi\_Cnt\_lgcSpecific Include Path present

No

# Runnable Scheduling

This section specifies the required runnable scheduling.

|  |  |  |
| --- | --- | --- |
| **Init** | **Scheduling Requirements** | **Trigger** |
| **None** |  |  |

|  |  |  |
| --- | --- | --- |
| **Runnable** | **Scheduling Requirements** | **Trigger** |
| **StOpCtrl\_Per1** | triggered on TimingEvent | RTE 2ms |

**.**

# Memory Map REQUIREMENTS

## Mapping

|  |  |  |
| --- | --- | --- |
| **Memory Section** | **Contents** | **Notes** |
| STOPCTRL\_START\_SEC\_VAR\_INIT\_08 |  |  |
| STOPCTRL\_START\_SEC\_VAR\_CLEARED\_32 |  |  |

\* Each …START\_SEC… constant is terminated by a …STOP\_SEC… constant as specified in the AUTOSAR Memory Mapping requirements.

## Usage

|  |  |  |
| --- | --- | --- |
| **Feature** | **RAM** | **ROM** |
| **None** |  |  |

Table 1: ARM Cortex R4 Memory Usage

## Non RTE NvM Blocks

|  |
| --- |
| **Block Name** |
| **None** |

Note : Size of the NVM block if configured in developer

## RTE NvM Blocks

|  |
| --- |
| **Block Name** |
| **None** |

Note : Size of the NVM block if configured in developer

# Compiler Settings

## Preprocessor MACRO

None

## Optimization Settings

None

# Appendix

*<This section is for appendix>*